

Fig. 1 (PRIOR ART)

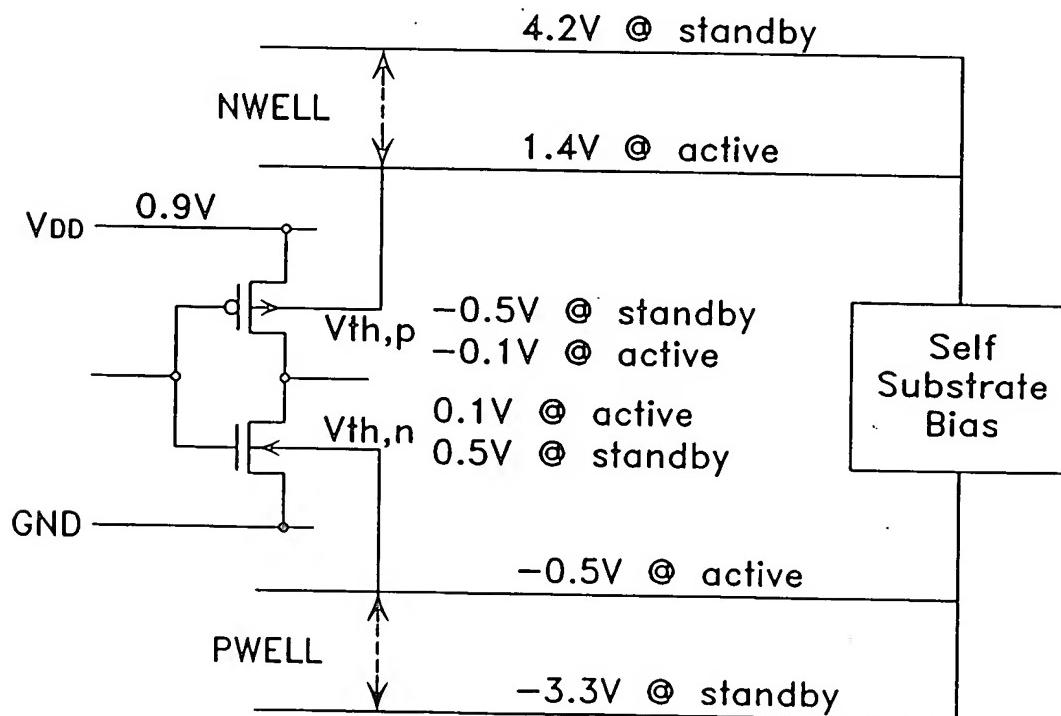


Fig. 2 (PRIOR ART)

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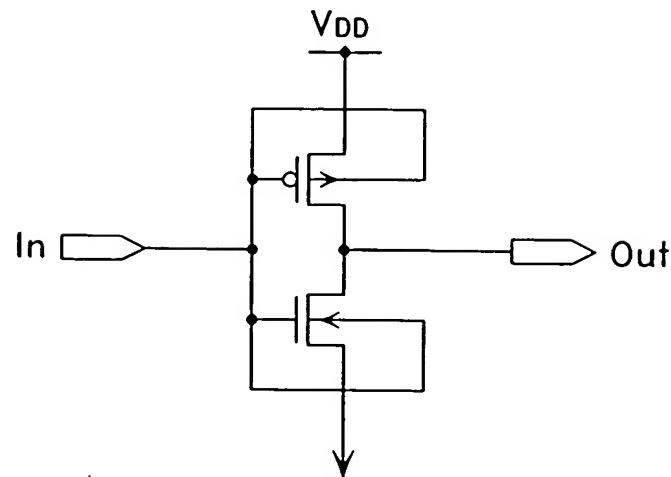
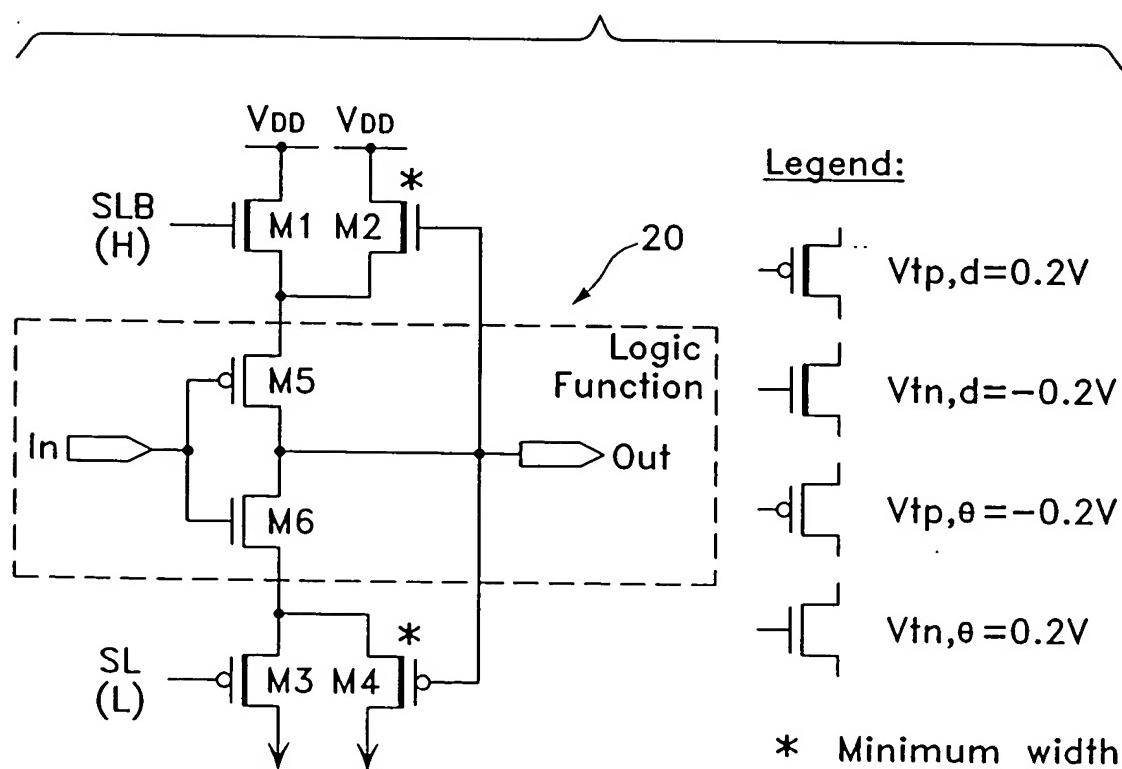


Fig. 3 (PRIOR ART)

Fig. 4



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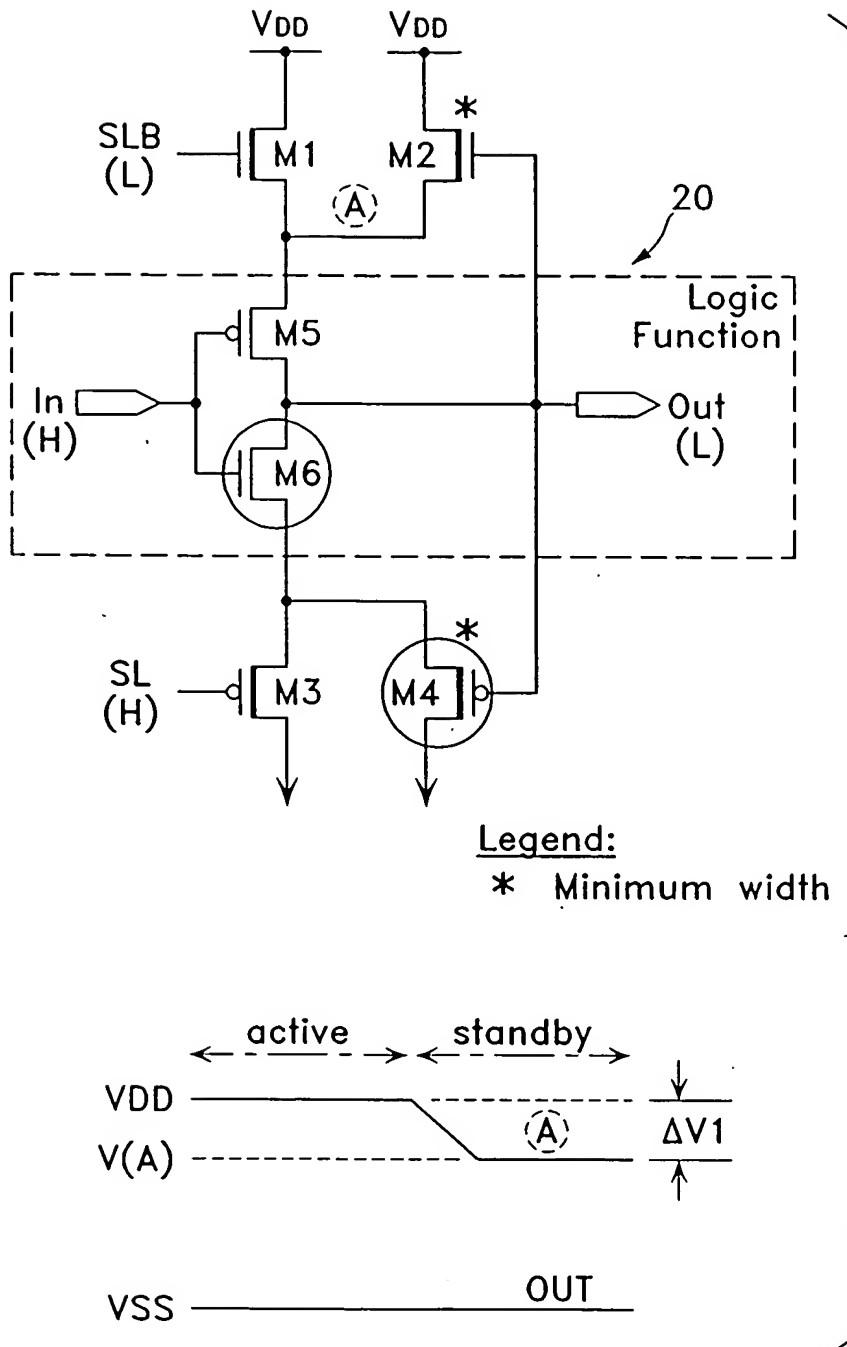


Fig. 5

1

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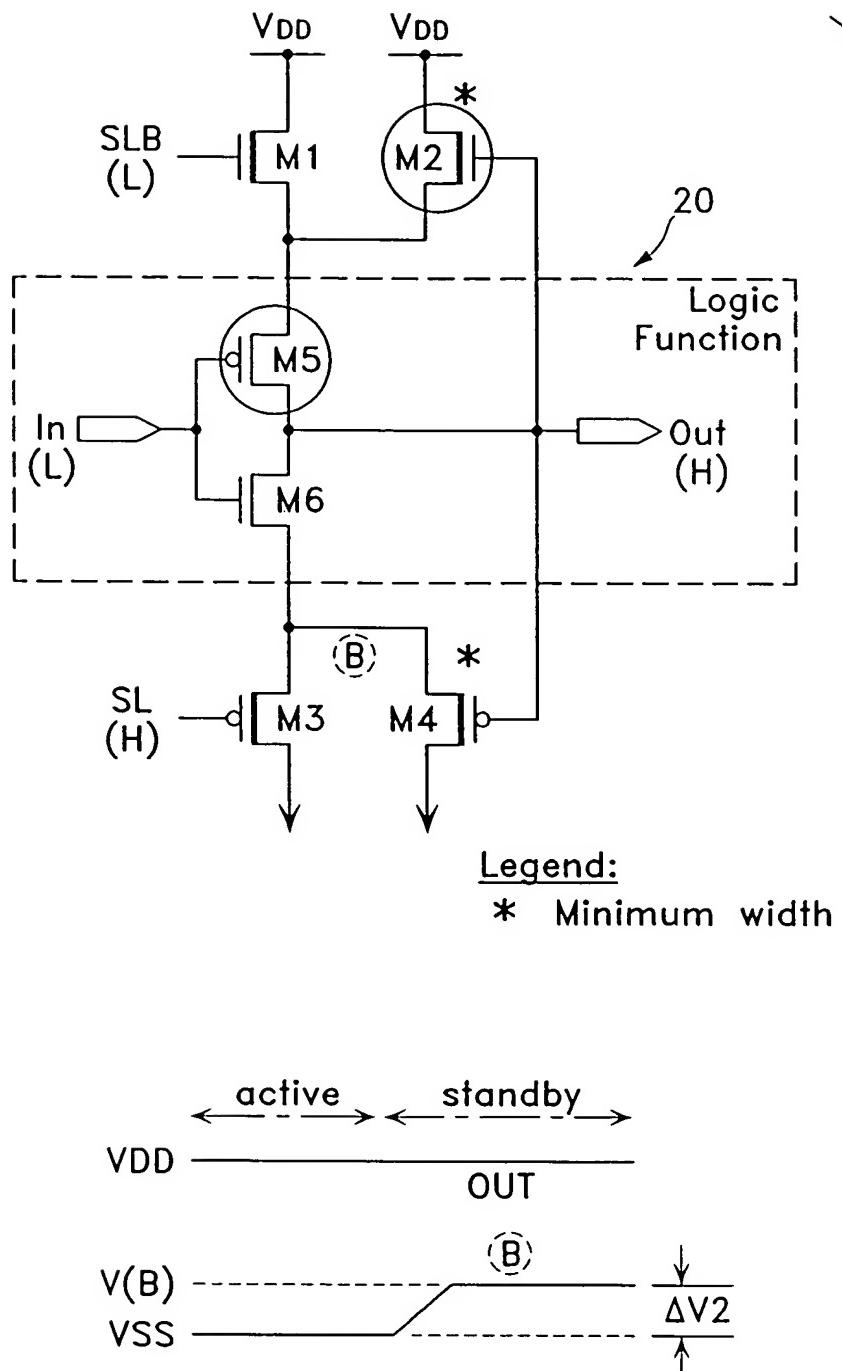


Fig. 6

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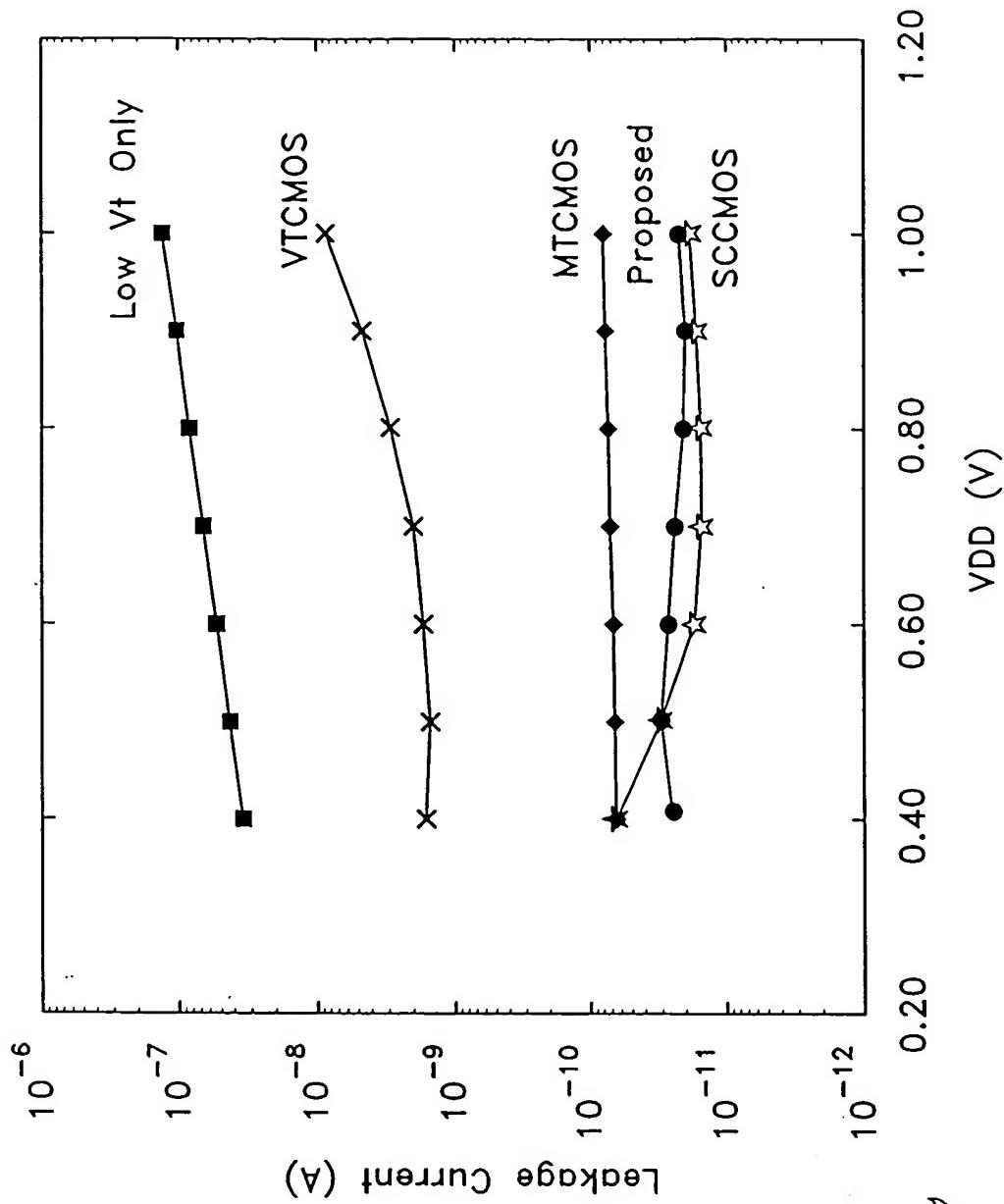


Fig. 7

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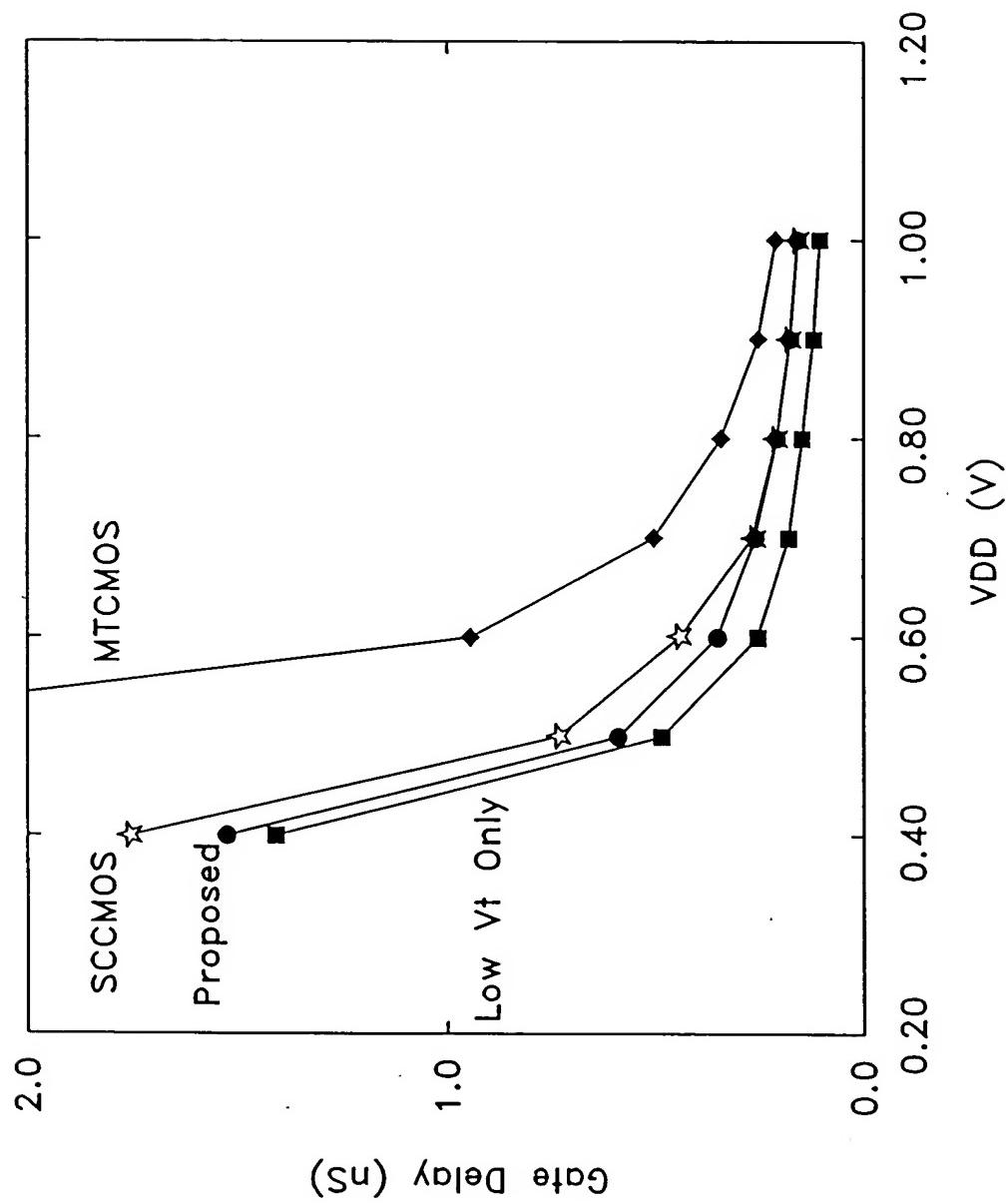


Fig. 8

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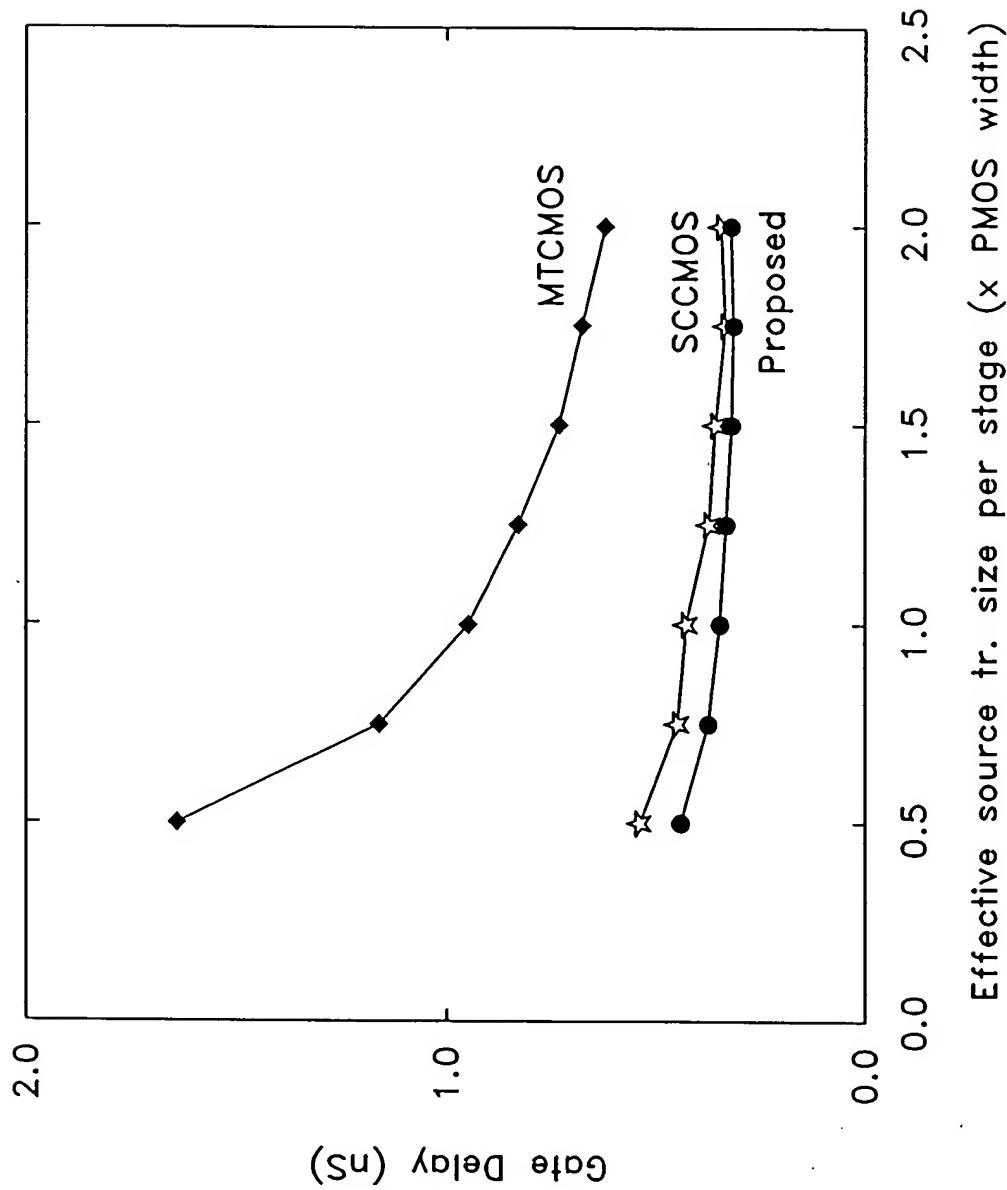


Fig. 9

□

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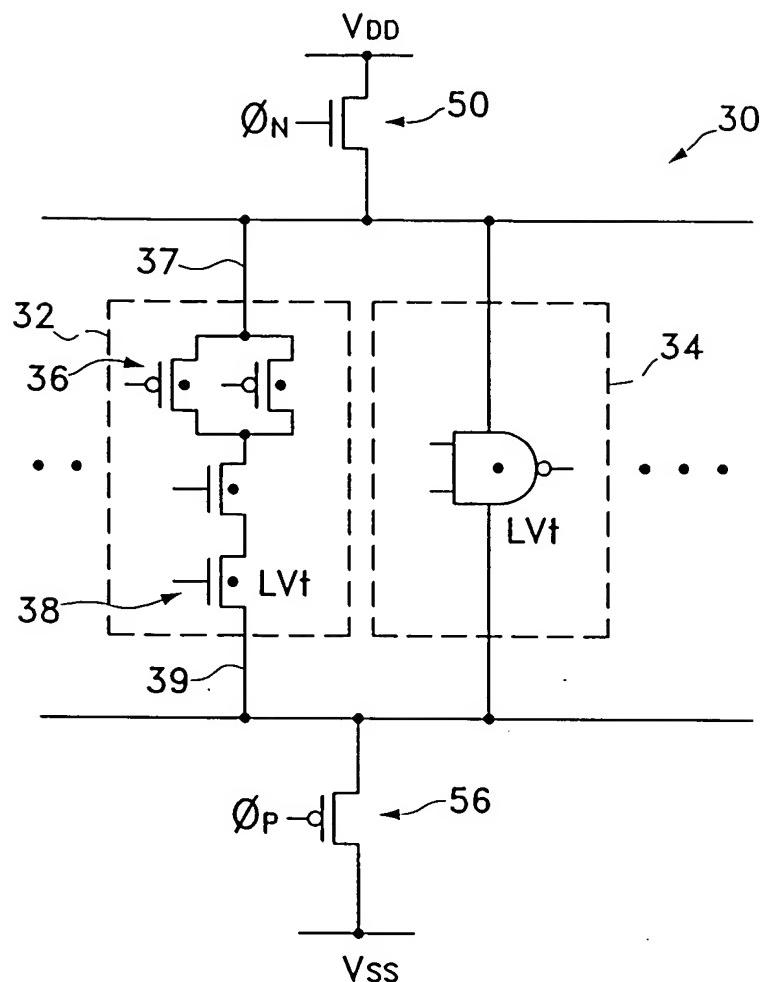


Fig. 10

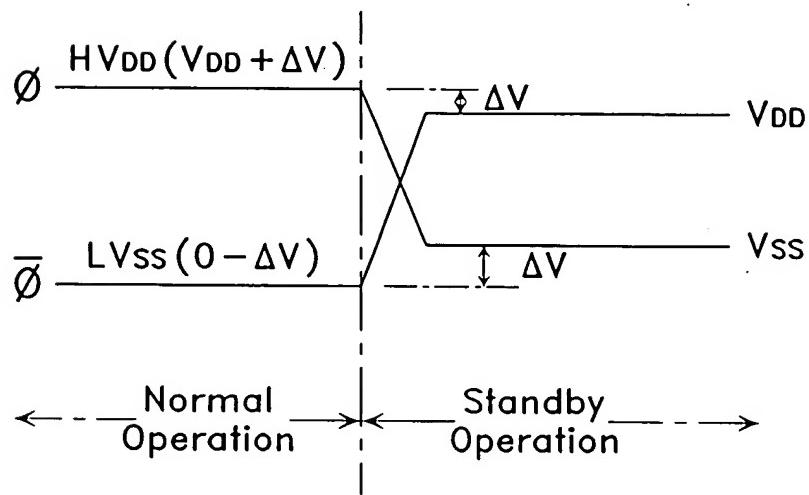


Fig. 11

□

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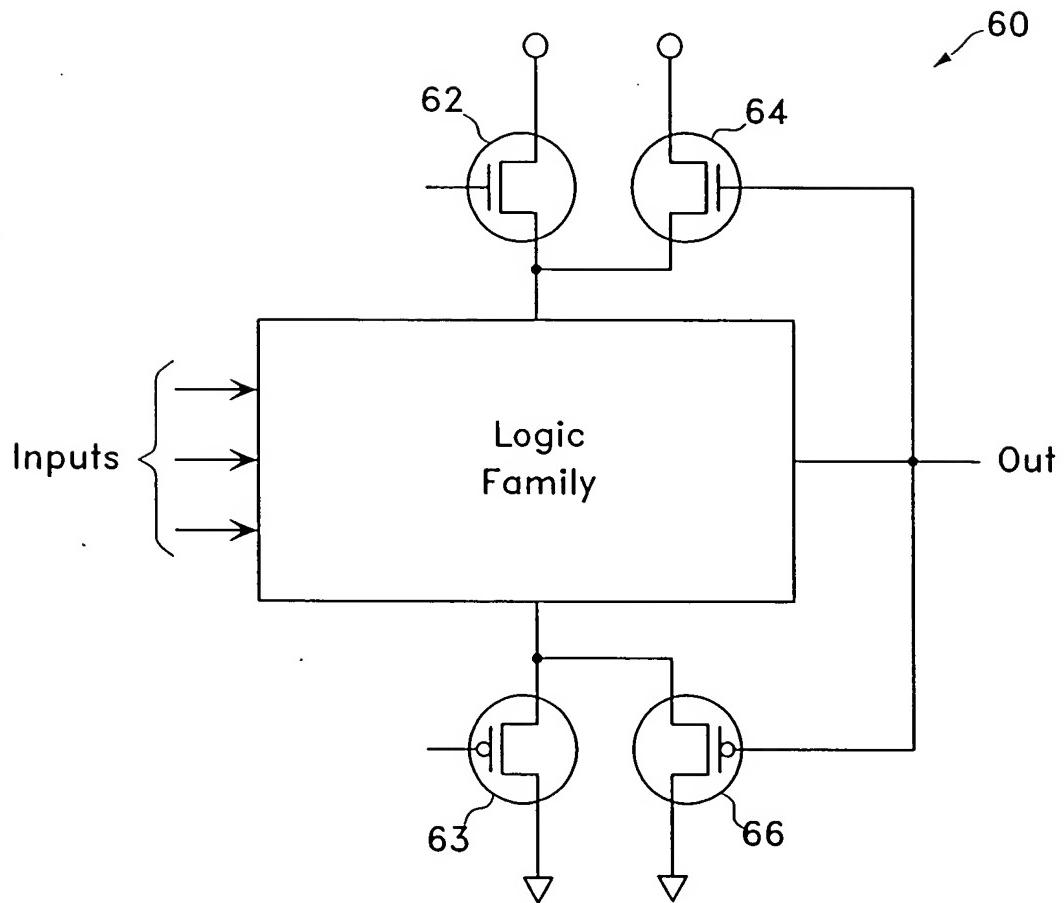


Fig. 12

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Example 1 – Conventional Logic Type

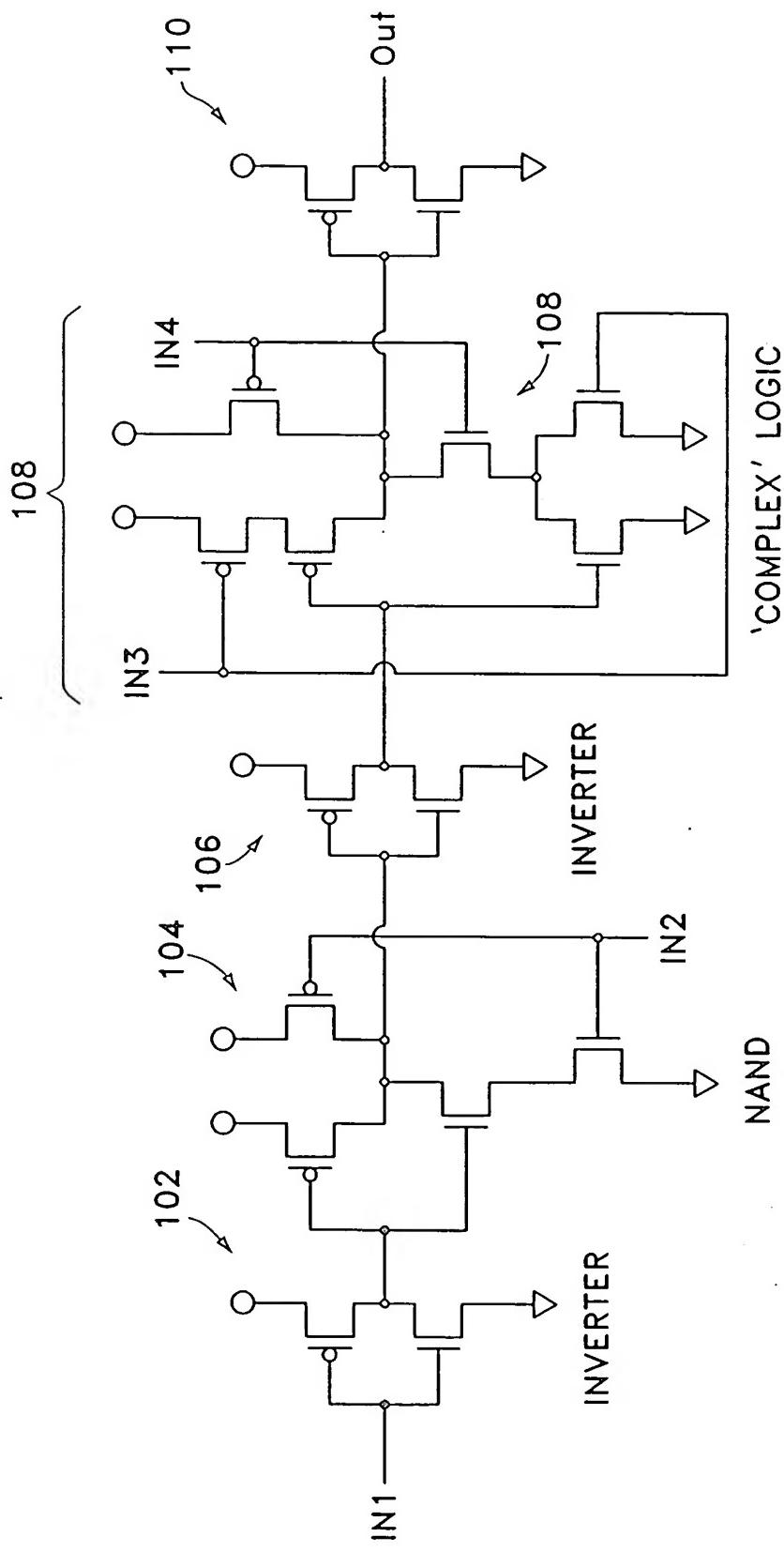


Fig. 13 (PRIOR ART)

